



Attorney Docket No. 5308-245

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: David B. Slater et al.

Application Serial No.: 10/659,241

Filed: September 9, 2003

For: PHOSPHOR-COATED LIGHT EMITTING DIODES INCLUDING TAPERED
SIDEWALLS, AND FABRICATION METHODS THEREFOR

November 11, 2003

Commissioner for Patents

P.O. Box 1450

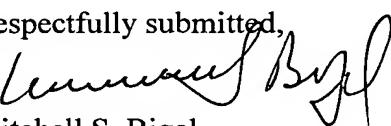
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 C.F.R. § 1.97(b)

Sir:

Attached is a form PTO-1449, together with a copy of each of the identified document(s). It is requested that these documents be considered by the Examiner and officially made of record in accordance with the provisions of 37 C.F.R. § 1.56 and Section 609 of the MPEP. This Information Disclosure Statement is submitted in accordance with 37 C.F.R. § 1.97(b), within three months of the filing date of the above-referenced application or before the mailing of a first Office Action on the merits, whichever event occurs last. Therefore, no fee is believed due. However, the Commissioner is hereby authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

Respectfully submitted,


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Susan E. Freedman
Date of Signature: November 11, 2003

INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

(use as many sheets as necessary)



Sheet 1 of 2

Complete if Known

Application Number	10/659,241
Filing Date	09/09/2003
First Named Inventor	David B. Slater, Jr.
Group Art Unit	
Examiner Name	

Attorney Docket Number 5308-245

U.S. PATENTS AND PATENT PUBLICATIONS

Examiner Initials*	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY
		Number	Kind Code (if known)		
1	US-2003/0006418	A1		Emerson et al.	01/09/2003
2	US-2002/0123164	A1		Slater, Jr. et al.	09/05/2002
3	US-6,391,231	B1		Evans et al.	05/21/2002
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	US-				

Examiner Signature

Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.


**INFORMATION DISCLOSURE
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Sheet 2 of 2

Complete if Known

Application Number	10/659,241
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	T
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Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T
	64	Craford, <i>Overview of Device Issues in High-Brightness Light-Emitting Diodes</i> , Chapter, <i>High Brightness Light Emitting Diodes: Semiconductors and Semimetals</i> , Vol. 48, Stringfellow et al. ed., Academic Press, 1997, pp. 47-63	
	65	Loh, <i>Power Surface Mount Light Emitting Die Package</i> , USSN 10/446,532, 05/27/2003	

Examiner Signature

Date Considered